3rd SEM./CSE/IT/ 2021(W)

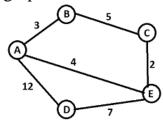
CST301 Data Structure

Full Marks: 80 Time- 3 Hrs Answer any five Questions including Q No.1& 2 Figures in the right hand margin indicates marks 1. Answer **All** questions 2 x10 State the need of a Data structure. a. Define queue. Write the applications of queue data structure. b. What are the advantages of linked list over array? c. d. Define degree of node in a graph. e. Define a complete binary tree f. Write any string function with example. What postfix expression is equivalent to the following infix g. expression? (A + B) - C * D / (E - F / G)How 2 dimensional array is represent in memory h. Define sparse matrix i. Explain overflow and underflow condition. į. 2. **Answer Any Six Questions** 5X6 Define data structure and discuss different type operation on data a. structure Discuss about the best case, worst case and average case complexity c. Define linear array. Write an algorithm to insert an element in a linear array. d. Define BST. Construct a binary search tree with 45, 15, 79, 90, 10,

55, 12, 20, 50

- e. Define stack. Write an algorithm for POP operation.
- f. Discuss about garbage collection.
- g Define linked list .Write an algorithm for traversing a linked list.
- Define graph .Discuss about adjacency matrix.

 Construct the adjacency matrix for the below undirected weighted graph?



- Define tree and discuss about different type of tree traversal with example.

 Define queue. What are the different types of queue? Write an 10
- Define queue. What are the different types of queue? Write an algorithm for inserting an element in a queue.
- Define searching. Write an algorithm for binary search and discuss it with an example.
- 7 Discuss about different collision resolution technique 10

III-SEM./CSE/ 2021(W) OLD CST- 302 Management Information System

Time- 3 Hrs

Full Marks: 80

Answer any five Questions including Q No.1& 2 Figures in the right hand margin indicates marks 1. Answer All questions 2×10 Write down the objectives of MIS. a. Define System Software. b. Define Office Automation. c. d. Define MODEM. What is E-Mail? e. f. What is TPS? Mention two advantages and two disadvantages of information system. What do you mean by Data communication? h. Define DBMS. i. į. Define the term GDSS. 2. Answer Any Six Questions 5X6 What is Network? Write down the types of Network. a. Explain DSS briefly. b. Describe the Framework of MIS and its Importance. Explain the generations of programming language. d. What are the methods used for organising data in files? Explain all. e. f. Differentiate between Primary Storage and Secondary Storage device. Explain the Components & Functions of a Client-Server System. 3 What is SDLC? Explain briefly with a Suitable diagram. 10 10 4 Describe the different layers of OSI Model. What is Data Base model? Write different types of data model & 5 10 explain with suitable diagram. Identify the basic Components of a computer system & explain them. 10 6 What is Network Topology? Explain the types of network topologies 7 10 with suitable diagram

3rd Sem./ E&TC/ AE & IE/CSE/ 2021(W) ETT321/ ETT 302 DIGITAL ELECTRONICS

Full Marks: 80		- 3 Hrs	
		Answer any five Questions including Q No.1& 2 Figures in the right hand margin indicates marks	
1.		Answer All questions	2 x 10
	a.	Subtract $(28)_{10}$ from $(39)_{10}$ by using 2's complement methods.	
	b.	Convert (0011011) from gray to binary.	
	c.	Define the term resolution and monotonicity.	
	d.	What is decoder and where it is used?	
	e.	Define don't care condition.	
	f.	Explain the term fan-in and fan-out.	
	g.	Perform excess-3 subtraction of (267-175).	
	h.	What is the difference between weighted and non-weighted codes?	
	i.	What is the meaning of Min. term and Max. term	
	j.	Define racing condition.	
2.		Answer Any Six Questions	5X6
	a.	Design a 1:4 de-multiplexer with a neat circuit diagram.	
	b.	With a neat diagram explain the operation of PIPO register.	
	c.	Explain with sketch the working of a TTL NAND gate.	
	d.	Simplify the Boolean expression	
		Y=AB+A(B+C)+B(B+C)	
		Draw the logic circuit for the simplified function.	
	e.	Explain the working of Full subtractor. Draw its circuit using any one ofuniversal gate.	
	f.	Differentiate between combinational and sequential circuit.	
	g	Design an 8:3 encoder with neat circuit diagram.	
3		With a neat diagram explain 4-bit ripple counter with its waveforms.	10
4		Which gates are referred to as universal gates and why? How other	10
		gates can be realized using NAND gates?	
5		Draw the logic diagram of Master-Slave JK flip flop. Explain its working with a functional table.	10
		Working With a fariotional table.	10
6		Simplify and minimize the four variable logic expression using k-map $F(A,B,C,D)=\Sigma M(1,5,7,8,9,10,11,14,15)$	
7		Explain with a neat sketch the successive approximation A/D	10

converter.

3rd Sem./ IT/CSE/ 2021(W)OLD ETT322 FUNDAMENTAL ELECTRONICS DEVICES

Full Marks: 80 Time-		3 Hrs	
		Answer any five Questions including Q No.1& 2 Figures in the right hand margin indicates marks	
1.		Answer All questions	2 x 10
	a.	What is biasing? Define forward and reverse biasing.	
	b.	Define doping. Name two n-type impurities.	
	c.	What is break down and knee voltage?	
	d.	State the difference between depletion and enhancement type	
		MOSFET.	
	e.	Define the transconductance and shorted gate drain current.	
	f.	Draw the symbols of MOSFET.	
	g.	Define and classify feedback amplifier.	
	h.	Write down two advantages of tuned amplifier.	
	i.	State the Barkhausen criteria.	
	j.	Why heat sinks are used in circuits?	
2.		Answer Any Six Questions	6 x 5
	a.	Explain the operation of photo transistor.	
	b.	With neat diagram explain construction and working of	
		complementary symmetry power amplifier.	
	c.	Establish the relation among JFET parameters.	
	d.	State and explain fundamental principle of working of oscillator.	
	e.	Derive the input and output impedance of negative feedback amplifier.	
	f.	Explain the working principle of single tuned amplifiers.	
	g	Differentiate between voltage and power amplifier.	
3		Describe the structure and mode of operation of MOSFET.	10
4		Derive the collector efficiency of class-A and class-B power amplifier.	10
5		With neat diagram explain the construction, working principle and	10
		advantages of push pull amplifier.	
6		Write short note on-	10
		a) Phase shift oscillator	
		b) Wein bridge oscillator	
7		Describe the pure and impure semiconductor.	10

III-SEM./CSE/IT/ 2021(W)

TH-I Computer System Architecture

Full Marks: 80		Γime- 3 Hrs	
		Answer any five Questions including Q No.1& 2 Figures in the right hand margin indicates marks	
1.		Answer All questions	2 x 10
	a.	What is computer architecture?	
	b.	What are the different types of field contain in an instruction format?	
	c.	Define indexed addressing mode.	
	d.	What is the requirement of page table?	
	e.	What is the use of RAID system?	
	f.	Define the types of micro operations.	
	g.	Define MIPS.	
	h.	Define MAR & MDR.	
	i.	Define hit ratio.	
	j.	What is parallel processing?	
2.		Answer Any Six Questions	5X6
	a.	Differentiate between SRAM & DRAM.	
	b.	Explain the different address instruction format.	
	c.	Comparison between I/O mapped I/O & memory mapped I/O.	
	d.	How an instruction is executed? Explain the steps of each cycle.	
	e.	Why cache memory is needed? Explain the mapping procedures cache memory.	s of
	f.	Explain five addressing modes with suitable example.	
	g	Explain the working principle USB protocol.	
3		What is pipelining? Draw the space time diagram to represent t	he 10
4		processing in a pipeline.	10
4		Define interrupt. Explain interrupt initiated I/O method of data	10
_		transfer to and from peripherals.	10
5		Describe the FLYNN's classification.	10
6		What is bus structure? Explain the basic parameter of bus desig	n. 10
7		Write the function of each type of bus.	10
7		Draw a functional block diagram of a computer and explain the	10
		function of each unit.	

III-SEM./ IT/CSE/ 2021(W)

TH-II Data Structure

Full Marks: 80

Time- 3 Hrs

		Answer any five Questions including Q No.1& 2 Figures in the right hand margin indicates marks	
1.		Answer All questions	2 x 10
	a.	What is the need of time-space trade off of an algorithm?	
	b.	State at least two applications of stack?	
	c.	Differentiate between array and string?	
	d.	What will be the output of following code	
		char str[]="Knowledge World"	
		printf("%d", strlen(str));	
		printf("%d", sizeof(str));	
	e.	Define ADT.	
	f.	Explain sparse matrix.	
	g.	What do you mean by polish notation? How it differs from two other notations?	
	h.	Differentiate between path matrix and adjacency matrix in a graph?	
	i.	Relate in between strict binary tree and complete binary tree.	
	j.	What will be the output of the following code	
		int arr[5]={10,20,30,70,90};	
_		printf("%d",a[5]);	
2.		Answer any five	5X6
	a.	Illustrate overflow and underflow status of Queue with example.	
	b.	Write a C program for linear search.	
	C.	Explain the procedure to insert a node at the end of a single linked list.	
	d.	Write the algorithm for binary search.	
	e. f.	How circular linked list differs from single linked list? Explain.	
		Discuss at least five string library functions with examples.	
	g	Analyse the memory representation of one-dimensional array with example.	
3		Write a C program to input and print a 3x2 dimensional matrix.	10
4		Explain different file organization and access methods?	10
5		Illustrate the push and pop operation of stack in evaluating an	10
		arithmetic expression.	10
6		Define binary tree. Discuss the traversing of a binary tree with example?	10
7		Write short notes on (any four)	10
-		a) Priority Queue c) Recursion e) Hashing	-
		b) BST d) Garbage Collection	

III-SEM,/ETC/AE&IE/CSE/IT/MECHATRONICS/ ELECTRICAL(INST & CTRL/ECE/ 2021(W) **TH-III Digital Electronics**

Answer any five Questions including Q No.1& 2 Figures in the right hand margin indicates marks

Time- 3 Hrs

Full Marks: 80

1. Answer All questions 2 x 10 State De-Morgan's theorems. a. Convert (10110101)₂ from binary to grey code. b. Find out the number of input lines, output lines and select lines in : (i)1:8 Dec. Mux (ii)16:1 Mux Write down the truth table of half subtractor. d. Define race-around condition in flip flop and suggest a method to overcome it. e. f. Difference between combination and sequential logic circuit.(any 4) Mention the type of flip flops used in : (i)Ripple Counter (ii)Shift Register g. Write the excitation table of D-flip flop. h. List down different types of: (i) Analog to Digital Convertors, (ii) Digital to i. **Analog Convertors** Define Fan In and Fan Out. į. 2. Answer **Any Six** Questions 6 x 5 Simplify the below given expression using Karnaugh's map and draw the logic a. circuit using logic gates. $F(a, b, c, d) = \sum m (0.2, 3, 4, 7, 9, 10, 11, 15) + d (1.6, 8)$ Explain the function of 4: 1 MUX with neat diagram and truth table. b. Design the operation of full adder with the help of truth table and circuit diagram. c. Design a JK flip flop using a RS flip flop. d. With neat circuit diagram, Explain the working of R-2R ladder type DAC. e. Write any 5 differences between SRAM and DRAM. f. Draw CMOS logic circuit of NAND and NOR gates. g Realize all the logic gates (NOT, AND, OR, NAND, NOR, XOR, XNOR) using 3 10 NAND gates only. Design a 2-bit magnitude comparator using logic gates. 10 4 5 Sketch the logic diagram of clocked JK Flip - Flop. Explain its working with 10 functional table. Explain briefly SISO, SIPO, PISO and PIPO shift register. 6 10 7 Design a mod-6 synchronous up counter. 10 1

3rd Sem./CSE/IT/ 2021(W) Th4 OBJECT ORIENTED METHODOLOGY

Time- 3 Hrs

Full Marks: 80

Answer any five Questions including Q No.1& 2 Figures in the right hand margin indicates marks 1. 2 x 10 Answer **All** questions What is byte code? b. What are the different types of variables in java? Explain the usage of try and catch clause. d. How will you find out the length of a string in java? Define a package? e. f. How Java supports platform independency? **Define Widening Type Casting** g. Write the difference between method and constructor. i. Define stream. Difference between Buffered Reader and Scanner class in Java 2. 5X6 **Answer Any Six Questions** Discuss about JVM. JRE, JDK. b. What are literals in Java? Mention their different types. Define stream. Discuss about Input Stream and Output Stream in Java. Distinguish between String and string buffer. Differentiate between Method Overloading and Method Overriding in e. Java. f. Discuss about Exception handling mechanism? Explain how to use a particular package in a Java program. Give example. 3 Define inheritance. Describe different forms of inheritance. Does Java 10 support multiple inheritances? 10 4 List out the looping statements available in Java. Explain with example. 5 10 Briefly explain the OOPS Concepts.. 6 What do you mean by Constructor? Discuss different types of 10 constructor with example. 7 10 Explain about Class, Objects and Methods in Java with an example program